

101037.655

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	16	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and ((code or program) near2 (block or segment or section)) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 09:31
L2	0	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and (li-jianhui.in. or etzion-orna.in.) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 09:32
L3	14	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and ((source or original) near3 register) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:27
L5	0	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and ((source or original) near3 register) and (input near2 instruction) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:01
L6	14	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and ((source or original) near3 register) and (input and output) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:02
L7	14	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and ((source or original) near3 register) and ((block or segment or section) near3 code) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:17

L8	0	L7 and (RFT or 'register format tracking')	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:11
L9	0	((format near2 conver\$4) near2 register) and (instruction near2 set) and (rft or 'register format tracking') and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:26
L10	0	(conver\$4 near3 instruction) and (binary near2 translat\$4) and (input same output) and (rft or 'register format tracking') and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:13
L11	0	(binary near2 translat\$4) and ('RFT' or 'register format tracking') and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:14
L12	786	('RFT' or 'register format tracking') and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:14
L13	0	('register format tracking') and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:14
L14	0	('register formats tracking') and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:14
L15	0	(binary near2 translat\$4) and (register near2 format near2 tracking) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:15

L16	0	(binary near2 translat\$4) and (registe\$2 near2 forma\$2 near2 track\$3) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:15
L17	14	L7 and (original or source) same (target or designat\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:17
L18	14	L7 and ((original or source) same (target or designat\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:18
L19	1	L7 and ((original or source) same (target or designat\$5)) and ((detect\$4 or determin\$6) near3 (differen\$4 or inconsist\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:23
L20	0	L7 and (input near3 register) and (output near3 register) and ((original or source) same (target or designat\$5)) and ((detect\$4 or determin\$6) near3 (differen\$4 or inconsist\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:24
L21	0	L7 and (input near3 register) and (output near3 register) and ((original or source) same (target or designat\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:24
L22	0	L7 and (input near3 register) and (output near3 register) and ((detect\$4 or determin\$6) near3 (differen\$4 or inconsist\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:25
L23	0	L7 and (input near3 register) and (output near3 register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:25

L24	0	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and ((output near2 format) and (input near2 format)) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:28
L25	22	(binary near2 translat\$4) and ((output near2 format) and (input near2 format)) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:30
L26	8	(binary near2 translat\$4) and ((output near2 format) and (input near2 format)) and register and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:28
L27	1	(binary near2 translat\$4) and (((output near2 format) and (input near2 format)) same register) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:30
S1	2	"20030140335"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 13:52
S2	0	(register near2 format) and ((multi\$3 near2 format) near2 registe\$2) and (source same target) and ((input near2 instruction) same (output near2 instruction)) and (mask\$3 or conver\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 13:55
S3	0	(register near2 format) and ((multi\$3 near2 format) near2 registe\$2) and (source same target) and ((input near2 instruction) same (output near2 instruction)) and (binary near3 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 13:56

S4	0	(register near2 format) and ((multi\$3 near2 format) near3 registe\$2) and (source same target) and (input near2 instruction) and (output near2 instruction) and (binary near3 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 13:57
S5	0	(register near2 format) and ((multi\$3 near2 format) near3 registe\$2) and (source same target) and 'instruction' and (binary near3 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 13:57
S6	0	(register near3 format) and ((multi\$3 near2 format) near3 registe\$2) and (source same target) and 'instruction' and (binary near3 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 13:57
S7	12	(register near3 format) and (source same target) and 'instruction' and (binary near3 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 14:08
S8	12	(register near3 format) and (source same target) and ((conver\$4 or chang\$3) near4 register) and (binary near3 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 15:00
S9	29	((format near2 conver\$4) near2 register) and (instruction near2 set) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:12

S10	10	(US-20030140335-\$ or US-20050086650-\$ or US-20040268094-\$ or US-20010023480-\$).did. or (US-6502115-\$ or US-6292815-\$ or US-6266769-\$ or US-6263426-\$ or US-6247116-\$ or US-4949291-\$).did.	US-PGPUB; USPAT	OR	OFF	2005/06/16 16:23
S11	1	S10 and (((compar\$4 or detect\$3) near2 differen\$3) near4 (register or format))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 16:24
S12	1	S10 and (((compar\$4 or detect\$3) near2 differen\$3) same (register or format))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 16:25
S13	1	S10 and ((compar\$4 or detect\$3) near2 differen\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 16:25
S14	0	((format near2 conver\$4) near2 register) and (instruction near2 set) and (((compar\$4 or detect\$3) near2 differen\$4) near3 (register or format)) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 16:27
S15	0	((format near2 conver\$4) near2 register) and (instruction near2 set) and ((compar\$4 or detect\$3) near2 differen\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 16:28
S16	4	((format near2 conver\$4) near2 register) and ((compar\$4 or detect\$3) near2 differen\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 16:34
S17	14	((format near2 conver\$4) near2 register) and ((compar\$4 or detect\$3) near4 differen\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/17 10:00

S18	9	S10 and (access near2 register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 19:43
S19	2	S10 and (mask)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/16 19:43
S20	3	((format near2 conver\$4) near2 register) and ((compar\$4 or detect\$3) near4 differen\$4) and 'mask' and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/17 10:00
S21	9	((((format near2 conver\$4) near2 register) near3 data) and (instruction near2 set) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:55
S22	7	((((format near2 conver\$4) near2 register) near3 data) and (instruction near2 set) and output and input and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 14:58
S23	0	((((source near4 target) near2 register) near3 format) and (translat\$3 near3 binary) and (output near3 format) and (input near3 format) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:01
S24	0	((((source near4 target) near3 format) same register) and (translat\$3 near3 binary) and (output near3 format) and (input near3 format) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:02

S25	0	((source near4 target) same format) same register) and (translat\$3 near3 binary) and (output near3 format) and (input near3 format) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 15:27
S26	10	((instruction near2 format) same (translat\$4 near3 code)) and (register same (target or source)) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 16:32
S27	0	((source or original) near3 (multi\$3 near2 format)) and target and (translat\$4 near3 code) and register and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 16:34
S28	2	((source or original) same (multi\$3 near2 format)) and target and (translat\$4 near3 code) and register and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/28 16:34
S29	41	(determin\$3 near3 (register near2 format)) and (detect\$3 (instruction near3 (differe\$4 or inconsist\$4))) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 10:39
S30	34	(determin\$3 near3 (register near2 format)) and (detect\$3 (instruction near3 (differe\$4 or inconsist\$4))) and (translat\$3 or conver\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 10:56
S31	13	(determin\$3 near3 (register near2 format)) and (detect\$3 (instruction near3 (differe\$4 or inconsist\$4))) and (translat\$3 or conver\$4) and source and target and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 10:46

S32	1	((determin\$3 near3 (register near2 format)) and (detect\$3 (instruction near3 (differe\$4 or inconsist\$4))) and ((translat\$3 or conver\$4) near2 instruction) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 10:47
S33	0	((format near2 conver\$4) near3 instruction) and (binary near2 translat\$4) and (output same input) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 13:21
S34	0	((format near2 conver\$4) near3 instruction) and (binary near2 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 13:23
S35	60	(conver\$4 near3 instruction) and (binary near2 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 13:23
S36	48	(conver\$4 near3 instruction) and (binary near2 translat\$4) and (input same output) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 10:12
S37	47	(conver\$4 near3 instruction) and (binary near2 translat\$4) and (input same output) and register and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 13:24
S38	25	(conver\$4 near3 instruction) and (binary near2 translat\$4) and (input same output) and ((source or original) near3 register) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 14:41

S39	19	(US-20010023480-\$ or US-20020073299-\$ or US-20030140335-\$ or US-20040268094-\$ or US-20050086650-\$ or US-20030126587-\$).did. or (US-3953833-\$ or US-4949291-\$ or US-5455955-\$ or US-6105129-\$ or US-6247116-\$ or US-6263426-\$ or US-6266769-\$ or US-6275920-\$ or US-6292815-\$ or US-6502115-\$ or US-6789181-\$ or US-6463582-\$ or US-6397379-\$).did.	US-PGPUB; USPAT	OR	OFF	2005/12/22 13:37
S40	2	S39 and (block near3 code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 13:38
S41	5	S39 and ((block or section or segment) near3 code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 13:39
S42	19	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/22 14:42
S43	18	((conver\$4 near3 (format or structure)) same instruct\$4) and (binary near2 translat\$4) and block and (@ad < "20020103" or @prad < "20020103" or @rlad < "20020103"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/23 09:58

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'binary translation' 'instruction format' register

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Dynamic and Transparent Binary Translation

In BOA, **binary translation** is transparent: As Figure 1 shows, ... BOA uses a statically scheduled, compressed **instruction format** similar to the IA-64 ...

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UQBT: Adaptable Binary Translation at Low Cost

A reusable, component-based **binary-translation** framework lets engineers quickly and ... **Register transfer lists (RTLs)** describe the machine **instructions**' ...

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9 The SPARC Instruction Formats

Write **instructions** are encoded using the **formats** shown in Figure 9.3. When the destination **register** is the Y **register**, the rd field is set to the 5-bit ...

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Resourceable and Retargetable Binary Translation

Binary translation is the process of automatically **translating** a **binary** executable ... **instructions**, and it writes a **binary** file in the required **format**. ...

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[PDF] Binary Translation and Architecture Convergence Issues for IBM ...

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architecture based on **binary translation** to a very long **instruction** ...

The **instruction format** of the VLIW **instructions** is based on a lim- ...

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[PDF] Method and apparatus for determining branch addresses in programs ...

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branch **instructions** in the executable code generated by **binary translation**. 2.

Linear lookup table: a sparse table is maintained, where each possible input ...

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[PDF] Using Dynamic Binary Translation to Fuse Dependent Instructions

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fect the **instruction** set and dynamic **binary translation**. In this section we briefly overview only ... tate **register** indirect addressing in the short **format**. ...

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Introduction to Computers (Aut 01)

Instruction format: 4 hex characters per **instruction** PQRS HexChar1 Opcode ...

and hence **translating** to **binary** would give the following sequence of bytes for ...

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[PDF] Dynamic Binary Translation for Accumulator-Oriented Architectures

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binary translation from one existing **instruction** set to. another, with code portability being ... **format** with destination **register** specifier provides both a ...

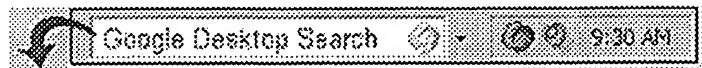
www.ece.wisc.edu/~jes/papers/cgo03.hskim.pdf - [Similar pages](#)

[Paper] A Methodology of **Binary**-Level Variable Analysis for ...
Toward this problem, we considered that the **binary translation** ... Hereafter,
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1 [Using Dynamic Binary Translation to Fuse Dependent Instructions](#)

Shiliang Hu, James E. Smith

 March 2004 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization CGO '04**

Publisher: IEEE Computer Society

 Full text available: [pdf\(240.50 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

Instruction scheduling hardware can be simplified and easily pipelined if pairs of dependent instructions are fused so they share a single instruction scheduling slot. We study an implementation of the x86 ISA that dynamically translates x86 code to an underlying ISA that supports instruction fusing. A microarchitecture that is co-designed with the fused instruction set completes the implementation. In this paper, we focus on the dynamic binary translator for such a co-designed x86 virtual machine. The dy ...

2 [Dynamic translation: Dynamic binary translation for accumulator-oriented architectures](#)

Ho-Seop Kim, James E. Smith

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Publisher: IEEE Computer Society

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A dynamic binary translation system for a co-designed virtual machine is described and evaluated. The underlying hardware directly executes an accumulator-oriented instruction set that exposes instruction dependence chains (strands) to a distributed microarchitecture containing a simple instruction pipeline. To support conventional program binaries, a source instruction set (Alpha in our study) is dynamically translated to the target accumulator instruction set. The binary translator identifies ...

3 [Binary translation and architecture convergence issues for IBM system/390](#)



Michael Gschwind, Kemal Ebcioglu, Erik Altman, Sumedh Sathaye

 May 2000 **Proceedings of the 14th international conference on Supercomputing**

Publisher: ACM Press

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We describe the design issues in an implementation of the ESA/390 architecture based on binary translation to a very long instruction word (VLIW) processor. During binary translation, complex ESA/390 instructions are decomposed into instruction "primitives"


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